

MBRP400100CTL

POWERTAP™ II SWITCHMODE™ Power Rectifier

These state-of-the-art devices use the Schottky Barrier principle with a platinum barrier metal.

Features

- Dual Diode Construction; May be Paralleled for Higher Current Output
- Guard-Ring for Stress Protection
- Low Forward Voltage Drop
- 175°C Operating Junction Temperature
- Recyclable Epoxy
- Guaranteed Reverse Avalanche Energy Capability
- Improved Mechanical Ratings
- Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	100	V
Working Peak Reverse Voltage	V_{RWM}		
DC Blocking Voltage	V_R		
Average Rectified Forward Current (At Rated V_R , $T_C = 100^\circ\text{C}$)	$I_{F(AV)}$	200 400	A Per Leg Per Device
Peak Repetitive Forward Current (At Rated V_R , Square Wave, 20 kHz, $T_C = 100^\circ\text{C}$)	I_{FRM}	400	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	2500	A
Peak Repetitive Reverse Current (2.0 μs , 1.0 kHz)	I_{RRM}	2.0	A
Storage and Operating Case Temperature Range	T_{stg}, T_C	-55 to +175	°C
Operating Junction Temperature	T_J	-55 to +175	°C
Voltage Rate of Change (Rated V_R)	dv/dt	1000	V/ μs

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

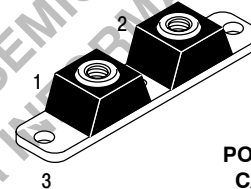
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ON Semiconductor®

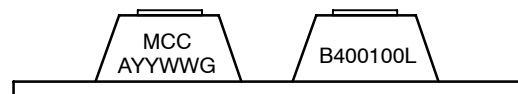
<http://onsemi.com>

SCHOTTKY BARRIER RECTIFIER 400 AMPERES, 100 VOLTS



POWERTAP II
CASE 357C
PLASTIC

MARKING DIAGRAM



B400100L = Specific Device Code
MCC = Mold Compound Code
A = Assembly Location
YY = Year
WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MBRP400100CTL	POWERTAP II	25 Units/Tray
MBRP400100CTLG	POWERTAP II (Pb-Free)	25 Units/Tray

MBRP400100CTL

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case Per Leg	$R_{\theta JC}$	0.45	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS

Rating	Symbol	Value		Unit
Maximum Instantaneous Forward Voltage (Note 1) Per Leg $(I_F = 200 \text{ A})$ $(I_F = 400 \text{ A})$	V_F	$T_C = 25^{\circ}\text{C}$	$T_C = 125^{\circ}\text{C}$	V
		0.83	0.69	
Maximum Instantaneous Reverse Current (Note 1) Per Leg $(\text{Rated DC Voltage})$	I_R	$T_C = 25^{\circ}\text{C}$	$T_C = 125^{\circ}\text{C}$	mA
		6.0	80	

1. Pulse Test: Pulse Width = 380 μs , Duty Cycle $\leq 2\%$.

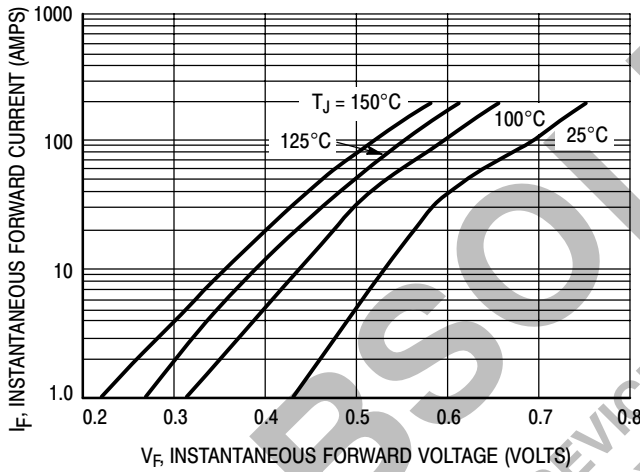


Figure 1. Typical Forward Voltage

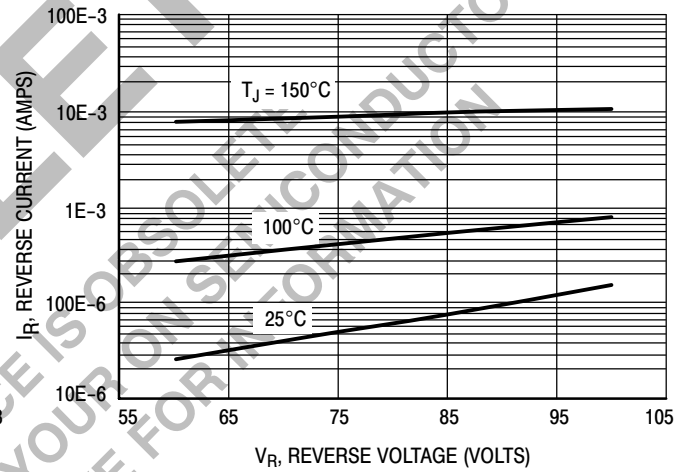
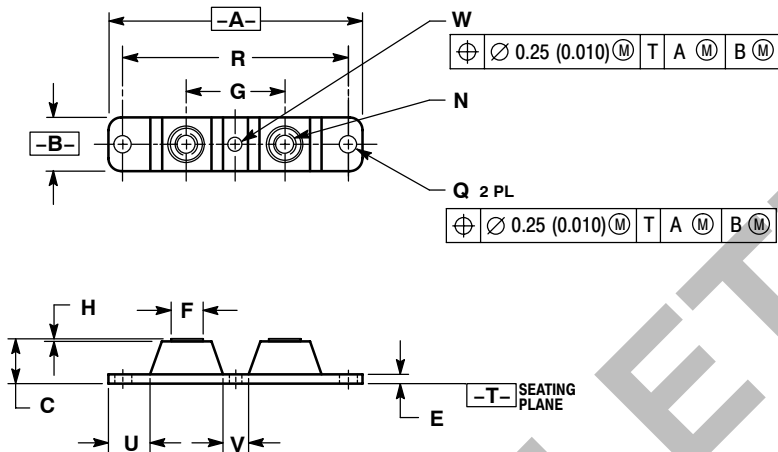


Figure 2. Typical Reverse Current

MBRP400100CTL

PACKAGE DIMENSIONS

CASE 357C-03
 POWERTAP
 PLASTIC PACKAGE
 ISSUE E



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. TERMINAL PENETRATION: 5.97 (0.235) MAXIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	3.450	3.635	87.63	92.33
B	0.700	0.810	17.78	20.57
C	0.615	0.640	15.63	16.26
E	0.120	0.130	3.05	3.30
F	0.435	0.445	11.05	11.30
G	1.370	1.380	34.80	35.05
H	0.007	0.030	0.18	0.76
N	1/4-20UNC-2B		1/4-20UNC-2B	
Q	0.270	0.285	6.86	7.23
R	31.50 BSC		80.01 BSC	
U	0.600	0.630	15.24	16.00
V	0.330	0.375	8.39	9.52
W	0.170	0.190	4.32	4.82

POWERTAP and SWITCHMODE are trademarks of Semiconductor Components Industries, LLC

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
 Literature Distribution Center for ON Semiconductor
 P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
 USA/Canada
Europe, Middle East and Africa Technical Support:
 Phone: 421 33 790 2910
Japan Customer Focus Center
 Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative