OCXO Model: OS400-1005-020

Issue 2; 17th May 2022

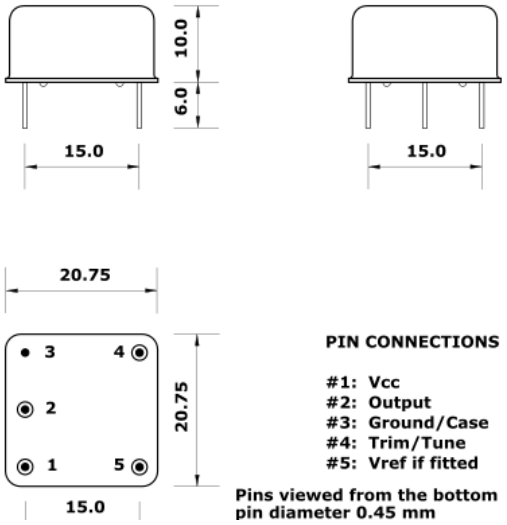
Features

- Temperature stability to ±20ppb
- Low phase noise
- Frequency 10MHz
- Industry standard package
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements

Option C

- Temperature stability: ±20ppb over (-40 to +70)°C
- Output: LVCMOS 15pF
- Duty cycle: 45% 55%
- Voltage: 5.0V
- Warm Up Current: 470mA
- Quiescent current: 220mA

Dimensions (mm)



Phase Noise (typical)

- $F0_0$+10Hz -125 dBc/Hz
- $F0_0$+100Hz -145 dBc/Hz
- $F0_0$+1KHz -155 dBc/Hz
- $F0_0$+10KHz -165 dBc/Hz
- $F0_0$+100KHz -168 dBc/Hz

Values based on a 10MHz unit

Voltage / Load change

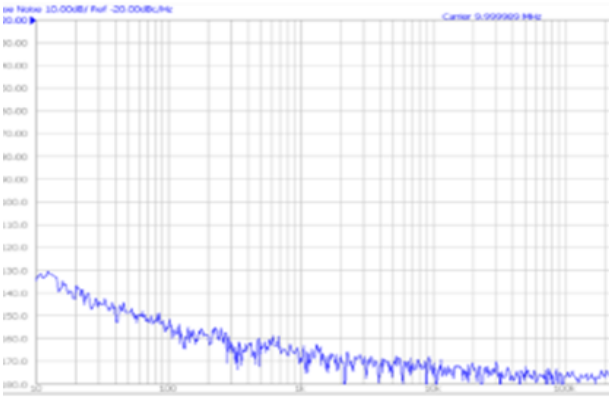
- ±5% supply voltage change: ±2ppb
- ±10% load change: ±10ppb

Ageing

Bases on 10MHz unit after 30 days continuous operation:

- Per day: ±0.7ppb max.
- Per year: ±200ppb max.
- Warm up time: 5 minutes to within 1 ppm

Phase Noise Plot



Voltage Trim

- ±0.5ppm minimum
- Trim impedance 50KΩ

Reference Options

- 3.0V

Environmental

- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range: (-40 to +125)°C

- Mechanical shock: MIL standard 202F, method 213, condition J
- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B
- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C

Compliance

- RoHS Status (2011/65/EU) - Compliant
- REACH Status - Compliant

Packaging

- Pack Style: Bulk

Ordering Information

- Unique customer part number and custom specification issued with each application
- OCXO Model: OS400-1005-020
- Frequency: 10MHz
- Stability/Output/Voltage: Option C
- Supply voltage code: V2 = +5.0Vd.c. supply
- Add suffix (R) for Vref output on pin #5

Test Circuit - Sinewave