

C1206C103JMRECTU

Aliases (C1206C103JMREC7800)

ESD SMD Comm X7R, Ceramic, 0.01 uF, 5%, 63 VDC, X7R, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II, 1206



Click [here](#) for the 3D model.

Dimensions

Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
T	0.78mm +/-0.10mm
B	0.5mm +/-0.25mm

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

General Information

Series	ESD SMD Comm X7R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	17 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	0.01 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	63 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	157.5 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	100 GOhms

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