

PCN Number:	20190304002.0	PCN Date:	March 06, 2019
Title:	Datasheet for TPS65094		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



TPS65094

SWCS133C – SEPTEMBER 2015 – REVISED FEBRUARY 2019

Changes from Revision B (February 2017) to Revision C	Page
• Changed TPS65094x to TPS65094 in title	1
• Deleted variants from top of each page	1
• Added "BUCK3-5 Mode" row and "TPS650945" column to <i>Summary of TPS65094x OTP Differences</i> table	6
• Changed the description of the VTTFB pin in the <i>Pin Functions</i> table	9
• Changed VSYS to PVIN in the efficiency graphs for BUCK3, BUCK4, and BUCK5 in the <i>Typical Characteristics</i> section	28
• Added to the description of the deassertion condition that causes an emergency shutdown in the <i>Emergency Shutdown</i> section	48
• Added TPS650945 settings to Section 6.6	53
• Changed OCP event to power fault event in the OCP bit description in the <i>OFFONSRC Register Field Descriptions</i> table	55
• Changed second reference of TPS650940 to TPS650944 for the bit reset values in the <i>LDOA2VID Register Field Descriptions</i> and <i>LDOA3VID Register Field Descriptions</i> tables.....	62
• Changed the bit values of the LDOA3_SLPVID[0] and LDOA3_VID[0] bits in the <i>LDOA3VID Register</i> figure.....	62

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS65094	SWCS133B	SWCS133C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TPS65094>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

TPS650940A0RSKR	TPS650940A0RSKT	TPS650941A0RSKR	TPS650941A0RSKT
TPS650942A0RSKR	TPS650942A0RSKT	TPS650944A0RSKR	TPS650944A0RSKT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
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