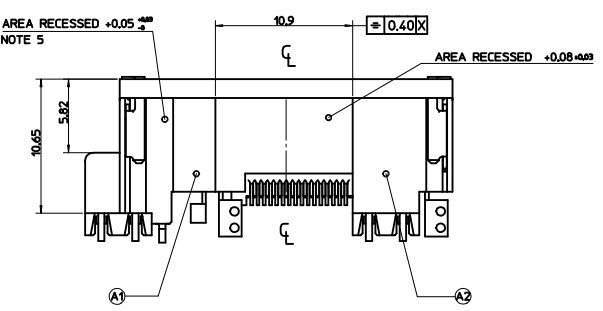
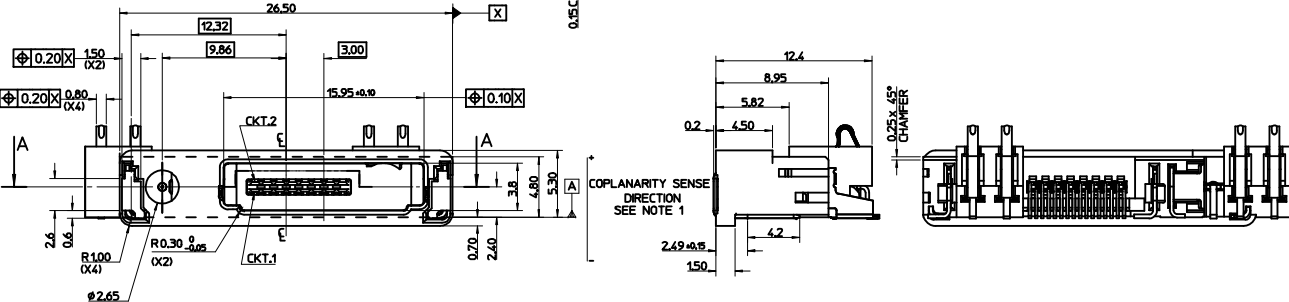


COMPONENT	RECOMMENDED PCB PAD DIMENSION	POSITION	QTY
SIGNAL CONTACT	0.35 +0.05/-0 X 2.0 (x16) $\phi$ D.10R	$\phi$ D.10X	16
CHARGER PADS	2.50 X 4.8 (x2) $\phi$ D.10R	$\phi$ D.30X	2
DC PIN	1.20 X 1.7 (x1) $\phi$ D.10R	$\phi$ D.30X	1
DC BEAM	1.75 X 3.1 (x1) $\phi$ D.10R	$\phi$ D.30X	1
LOCKING LATCHES	2.50 X 4.0 (x2) $\phi$ D.10R	$\phi$ D.30X	2
BATTERY CONTACT	1.10 X 1.8 (x4) $\phi$ D.10R	$\phi$ D.30X	4



COMPONENT	DESCRIPTION	QTY
HOUSING	45% GLASS FILLED NYLON 46 (RATED UL94 V-0), COLOUR BLACK.	1
SIGNAL CONTACT	MATERIAL: 0.20 REF. THK. PHOSPHOR BRONZE. PLATING: 127um MIN. NICKEL UNDERCOAT ALL OVER, 0.1um MIN. GOLD OVER 10um MIN. PALLADIUM NICKEL ON CONTACT AREA, 3.0um MIN. TIN IN SOLDER TAIL AREA (ALL SIDES).	16
CHARGER PADS/ SMT RETENTION NAILS	MATERIAL: 0.30 REF. THK. PHOSPHOR BRONZE. PLATING: 127um MIN. NICKEL UNDERCOAT ALL OVER, 0.1um MIN. GOLD FLASH OVER 10um MIN. PALLADIUM NICKEL IN CONTACT AREA, 3.0um MIN. TIN IN SOLDER TAIL AREA (ALL SIDES).	2
DC PIN	MATERIAL: 0.65 REF. THK. COPPER ALLOY. PLATING: 127um MIN. NICKEL UNDERCOAT ALL OVER, 3-4um NICKEL IN CONTACT AREA, 3.0um MIN. TIN IN SOLDER TAIL AREA.	1
DC BEAM	MATERIAL: 0.28 REF. THK. BERYLLIUM COPPER. PLATING: 0.25um MIN. NICKEL UNDERCOAT ALL OVER, 2-3um NICKEL UNDER 3um MIN SILVER, ONE SIDE ONLY, 3.0um MIN. TIN IN SOLDER TAIL AREA (ALL SIDES).	1
LOCKING LATCHES	MATERIAL: 0.30 REF. THK. STAINLESS STEEL. PLATING: 3-5um TIN OVER 127 -250um NICKEL UNDERCOAT ALL OVER.	2
BATTERY CONTACT	MATERIAL: 0.17 REF. THK. PHOSPHOR BRONZE. PLATING: 127um MIN. NICKEL UNDERCOAT ALL OVER, 0.1um MIN. GOLD FLASH OVER PALLADIUM-NICKEL (80/20) 1.0-15um ON CONTACT AREA, 3.0um MIN. Sn IN SOLDER TAIL AREA.	4

- NOTES:
1. COPLANARITY: ALL SMT TAILS TO LIE WITHIN A COPLANARITY BAND OF -0.05 TO -0.15 FROM DATUM 'A'. SEE COPLANARITY SENSE DIRECTION. DATUM 'A' IS TO BE TAKEN AS THE PLANE OF A METAL BLOCK ON WHICH DATUM A1 & A2 REST.
  2. PACKAGING: TAPE AND REEL. SEE DRAWING SD-91403-005
  3. LEAD FLATNESS: 0.1 FOR ALL SMT TAILS
  4. TEXT TO BE LASER PRINTED
  5. GATING VESTIGE MARK NOT TO PROTRUDE PAST DATUM A
  6. TEXT: PART NO./ MX LD./ CAVITY LD.. TO BE 1.0 HIGH AND RAISED 0.1 MAX

REMOVE LEAD FROM PLATING EC NO. E2004-0574 DRW: DCARTON 04.02.26 CHK: CHY APPR:	QUALITY SYMBOLS	GENERAL TOLERANCES: (UNLESS SPECIFIED)	SCALE 5:1	DESIGN UNITS	DIMENSIONS:	SHT	REV
	MAJOR = $\nabla$ CRITICAL = $\nabla$	(mm) INCH	DRAWN BY & DATE dcarton 00.07.19	THIRD ANGLE PROJECTION	mm INCH mm ONLY	REVISE ON CAD ONLY	
	4 PLACES ±--- ±---		CHECKED BY & DATE jhorgan 99.08.10	TITLE: MOBILE PHONE I/O CONNECTOR			
	3 PLACES ±--- ±---		APPROVED BY & DATE mwhite 99.08.10	MATERIAL NO. 91403-0001			
	2 PLACES ±0.05 ±---		CAD FILENAME	DRAWING NO. SD-91403-001			
	1 PLACE ±0.10 ±---			SHEET NO. 1 of 1			
	ANGULAR: ± 1/2°						
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS					

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