



**NOTES: UNLESS OTHERWISE SPECIFIED**

1. ALL RESISTORS ARE IN OHMS, 0402.
2. INSTALL SHUNTS ON JP1 PIN 2 AND 3, JP2 PIN 1 AND 2.

\* C1 IS AN OPTIONAL PART. IT IS INSERTED ON THE DC687A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE, DUE TO THE INDUCTANCE OF LONG INPUT CABLES. ON NORMAL, TYPICAL PCBS, WITH SHORT TRACES, C1 IS NOT NEEDED.

<p><b>CUSTOMER NOTICE</b></p> <p>LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.</p> <p>THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.</p>	CONTRACT NO.		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only		
	APPROVALS		<b>LOW VOLTAGE VLDO LINEAR REGULATOR</b>		
	DRAWN: KIM T.	TITLE: SCHEMATIC			
	CHECKED:	APPROVED:	SIZE A	DWG NO. <b>DC687A-1 * LT3020DD8</b>	REV A
	ENGINEER: TOM G.	DESIGNER:	DATE: Tuesday, June 15, 2004	SHEET 1 OF 1	